

Abstract

An analysis facility analyzes simulation output resulting from a simulator of an electrical component, such as a microprocessor. The simulation output contains

- 5 waveform representations of at least one data signal and a clock signal. The analysis facility analyzes the waveform representations and the simulation output to produce a report regarding signaling performance metrics, such as data jitter, setup times and hold times. The analysis facility may be customized by the user specifying parameters. For example, the user may specify what portion of the time frame for which simulation
- 10 results produced are to be analyzed by the analysis facility. The user may also specify parameters such as cycle time and reference voltage values, including an indeterminate state region.

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